register has a variable writing speed of data, and successively writes and outputs the input signal from the memory at the specified speed,

the spread code register is prepared for the necessary number of users, and cyclically shifts and outputs the generated spread code, and

the controller controls a cyclic shift in said spread code register in accordance with a writing position in the input signal register.

7. (amended) The correlation circuit for spread spectrum communication according to claim 3 wherein the input signal register has a variable writing speed of data, and successively writes and outputs the input signal from the memory at the specified speed,

the spread code register is prepared for the necessary number of users, and cyclically shifts and outputs the generated spread code, and

the controller controls a cyclic shift in said spread code register in accordance with a writing position in the input signal register.

8. (amended) The correlation circuit for spread spectrum communication according to claim 4 wherein the input signal register has a variable writing speed of data, and successively writes and outputs the input signal from the memory at the specified speed,

the spread code register is prepared for the necessary number of users, and cyclically shifts and outputs the generated spread code, and

the controller controls a cyclic shift in said spread code register in accordance with a writing position in the input signal

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register.

9. (amended) The correlation circuit for spread spectrum communication according to claim 1 wherein the sum of products calculator comprises a multiplier section for multiplying the input signal and the spread code, and an adder section for successively adding a multiplication result in a stepwise manner,

a plurality of outputs in the course of stepwise addition in said adder section can be selectively outputted as partial correlation outputs, and

the controller selects the partial correlation output in said adder section in accordance with a spread ratio of said spread code.

10. (amended) The correlation circuit for spread spectrum communication according to claim 2 wherein the sum of products calculator comprises a multiplier section for multiplying the input signal and the spread code, and an adder section for successively adding a multiplication result in a stepwise manner,

a plurality of outputs in the course of stepwise addition in said adder section can be selectively outputted as partial correlation outputs, and

the controller selects the partial correlation output in said adder section in accordance with a spread ratio of said spread code.

11. (amended) The correlation circuit for spread spectrum communication according to claim 3 wherein the sum of products calculator comprises a multiplier section for multiplying the input signal and the spread code, and an adder section for successively adding a multiplication result in a stepwise manner,

a plurality of outputs in the course of stepwise addition in

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said adder section can be selectively outputted as partial correlation outputs, and

the controller selects the partial correlation output in said adder section in accordance with a spread ratio of said spread code.

12. (amended) The correlation circuit for spread spectrum communication according to claim 4 wherein the sum of products calculator comprises a multiplier section for multiplying the input signal and the spread code, and an adder section for successively adding a multiplication result in a stepwise manner,

a plurality of outputs in the course of stepwise addition in said adder section can be selectively outputted as partial correlation outputs, and

the controller selects the partial correlation output in said adder section in accordance with a spread ratio of said spread code.

13. (amended) The correlation circuit for spread spectrum communication according to claim 1 wherein the sum of products calculator comprises a multiplier section for multiplying the input signal and the spread code, and an adder section for successively adding a multiplication result in a stepwise manner, and

the controller resets an adder for performing stepwise addition in the adder section in accordance with the spread ratio of the spread code.

14. (amended) The correlation circuit for spread spectrum communication according to claim 2 wherein the sum of products calculator comprises a multiplier section for multiplying the input signal and the spread code, and an adder section for successively adding a multiplication result in a stepwise manner, and

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the controller resets an adder for performing stepwise addition in the adder section in accordance with the spread ratio of the spread code.

15. (amended) The correlation circuit for spread spectrum communication according to claim 3 wherein the sum of products calculator comprises a multiplier section for multiplying the input signal and the spread code, and an adder section for successively adding a multiplication result in a stepwise manner, and

the controller resets an adder for performing stepwise addition in the adder section in accordance with the spread ratio of the spread code.

16. (amended) The correlation circuit for spread spectrum communication according to claim 4 wherein the sum of products calculator comprises a multiplier section for multiplying the input signal and the spread code, and an adder section for successively adding a multiplication result in a stepwise manner, and

the controller resets an adder for performing stepwise addition in the adder section in accordance with the spread ratio of the spread code.

17. (amended) The correlation circuit for spread spectrum communication according to claim 1 wherein the sum of products calculator comprises the multiplier section constituted of a plurality of multipliers for multiplying the input signal and the spread code, and a plurality of block adder sections for outputting the partial correlation outputs added by a unit of the specified number of multipliers in said multiplier section, and

the controller selects the partial correlation output from said block adder section in accordance with the spread ratio of said spread code, and adds said selected partial correlation

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outputs when there are a plurality of selected partial correlation outputs.

18. (amended) The correlation circuit for spread spectrum communication according to claim 2 wherein the sum of products calculator comprises the multiplier section constituted of a plurality of multipliers for multiplying the input signal and the spread code, and a plurality of block adder sections for outputting the partial correlation outputs added by a unit of the specified number of multipliers in said multiplier section, and

the controller selects the partial correlation output from said block adder section in accordance with the spread ratio of said spread code, and adds said selected partial correlation outputs when there are a plurality of selected partial correlation outputs.

19. (amended) The correlation circuit for spread spectrum communication according to claim 3 wherein the sum of products calculator comprises the multiplier section constituted of a plurality of multipliers for multiplying the input signal and the spread code, and a plurality of block adder sections for outputting the partial correlation outputs added by a unit of the specified number of multipliers in said multiplier section, and

the controller selects the partial correlation output from said block adder section in accordance with the spread ratio of said spread code, and adds said selected partial correlation outputs when there are a plurality of selected partial correlation outputs.

20. (amended) The correlation circuit for spread spectrum communication according to claim 4 wherein the sum of products calculator comprises the multiplier section constituted of a

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plurality of multipliers for multiplying the input signal and the spread code, and a plurality of block adder sections for outputting the partial correlation outputs added by a unit of the specified number of multipliers in said multiplier section, and

the controller selects the partial correlation output from said block adder section in accordance with the spread ratio of said spread code, and adds said selected partial correlation outputs when there are a plurality of selected partial correlation outputs.